

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Inventor:	<b>Kai D. Feng et al.</b>	Date:	<b>November 14, 2003</b>
Application No.:		Examiner:	
Filed:	<b>Herewith</b>	Art Unit:	
Title:	<b>Dual Edge Programmable Delay Unit</b>	Attorney:	<b>Graham S. Jones, II 42 Barnard Avenue Poughkeepsie, NY 12601-5023</b>

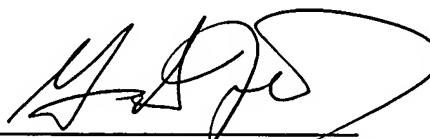
**INFORMATION DISCLOSURE STATEMENT**

**Commissioner for Patents  
P.O. Box 1450  
Alexandria VS, 22313-1450**

**Sir:**

Pursuant to the duty of disclosure set forth in 37 CFR 1.56, and further pursuant to 37 CFR 1.97 and 37 CFR 1.98, Applicants hereby respectfully identify prior patents and published applications and submit copies of the publications as listed on Form PTO-1449, attached hereto.

**Respectfully submitted,  
Kai D. Feng et al.**

By:   
**Graham S. Jones, II, Attorney  
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**Enclosures: Form 1449 with cited publications attached**

<b>FORM PTO-1449 (Modified)</b>  <b>LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT</b>  <b>(Use several sheets if necessary)</b>	<b>Attorney Docket Number:</b>  <b>END920030085US1</b>	<b>Serial Number</b>
	<b>APPLICANT: Kai D. Feng et al.</b>	
	<b>Filing Date:</b>	<b>Group:</b>

Examiner Initials		Document Number	Issued Date	Name	Class	Subclass	FILING DATE (IF APPRO.)
	AA	5,933,039	3 Aug. 1999	Hui	327	262	
	AB	5,936,451	10 Aug. 1999	Phillips	327	285	
	AC	5,355,038	11 Oct. 1994	Hui	307	603	
	AD	6,124,745	26 Sep. 2000	Hilton	327	276	
	AE	6,031,401	29 Feb. 2000	Dasgupta	327	116	
	AF	6,326,813	4 Dec. 2001	Lindsay	326	93	
	AG	5,572,159	5 Nov. 1996	McFarland	327	276	
	AH	5,175,452	29 Dec. 1992	Lupi	307	591	
	AI	4,888,062	11 Dec. 1984	Weizhofer	307	268	

		Published Application No.	Publication Date	Name	Class	Subclass	
	AJ	US2003/0112045A1	19 Jun. 2003	Atallah	327	175	
	AK	US2002/0030524A1	14 Mar. 2002	Fifield	327	278	

**FOREIGN PATENT DOCUMENTS**

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AK							

**OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)**

	AL	"Reduced Power Delay Line Implementation" IBM TDB Vol. 36 No. 12 Dec. 1993 pp. 277-279					
	AM	"Variable Shift Delay Book" IBM TDB Vol. 34 No. 6 Nov. 1999 pp. 312-313					

**EXAMINER****DATE CONSIDERED**

**EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.